

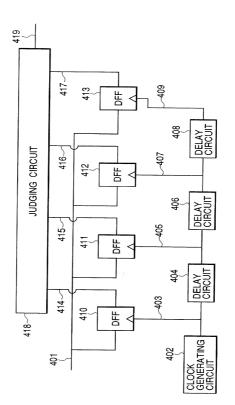
327

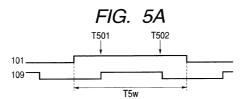
325

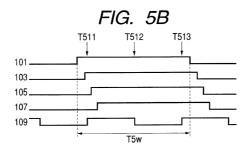
324 H H -309 323 JUDGING CIRCUIT DELAY CIRCUIT 322 DFF DFF -307 FIG. 3 -321 DELAY 306 320 312 H PF 305 319 DELAY 304 318 310 PFF H 326~ -303 CLOCK GENERATING CIRCUIT 30 302

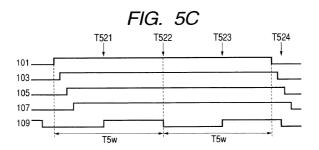
3/10

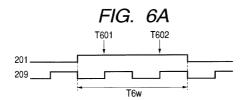
FIG. 4

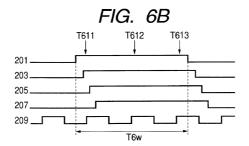


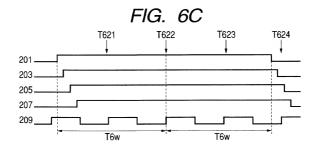


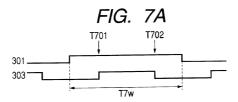














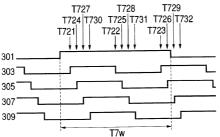
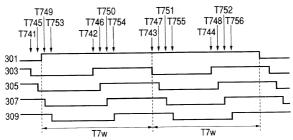


FIG. 7C



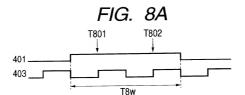
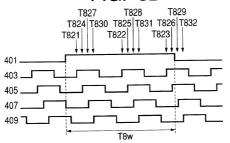


FIG. 8B





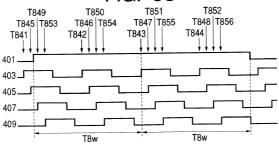


FIG. 9 PRIOR ART

